Triple Dimmer Module

Intellitec's Triple Dimmer Module provides full range dimming control of up to three individual 7.5 Amp lamp loads. The dimmer operates using pulse-width modulation to smoothly reduce the voltage in the most efficient way to minimize heating of the components. The brightness of the lamps is controlled by applying a variable voltage from an external potentiometer to the control input. The output voltage will be approximately proportional to the input voltage. Typically, this input is provided from a potentiometer mounted on a remote control panel.

The three circuits of the dimmer can be operated individually or the control inputs can be paralleled so that one pot can control all three loads. This allows the dimmer to control up to 22.5 Amps of load.

Each of the three outputs is fused separately to protect them from inadvertent shorts. Connections are made to the module through an AMP Mate-N-Lok connector on the module.



Part Number 00-00661-000



www.intellitec.com

P/N 53-00661-000 Rev. B 040519

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How It Works:

The Triple Dimmer Module is designed to control up to three individual lamp loads. It reduces the voltage to the lamp through pulse-width modulation. Full voltage is applied to the lamp for a period of time and then turned off for a period of time. This action happens hundreds of times per second so the effect is to produce "constant" level of light. The percentage of on to off time effectively reduces the voltage to the lamp.

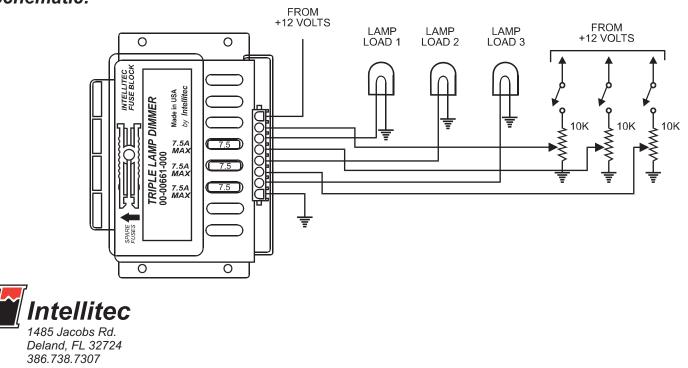
Leaving a control line input floating will cause the output of that channel to be at half voltage.

Specifications:

Part Number Maximum Continuous Carry Current Maximum Ambient Temperature Minimum Ambient Temperature Normal Input Voltage Range: Short Term Over Voltage Protection: Reverse Voltage Protection: Positive Voltage Spike Protection: Operating Environment: 00-00661-000 7.5 Amps Max. per channel 185 Degrees F. -40 Degrees F. 10 to 16 volts +24 volts - 300 volts +150 volts Electronics assembly out of direct weather >100K ohms

Control Input resistance

Schematic:



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